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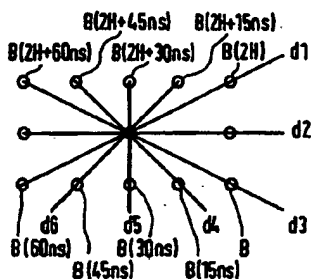
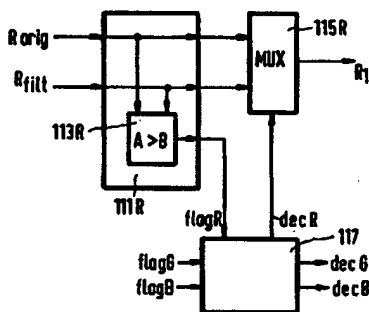
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NL-5656 AA Eindhoven (NL)(54) **Signal correction.**

(57) A signal correction circuit for correcting deviating pixel values comprises a part (113, 117) for comparing a first pixel value (R_{orig}) of a given pixel with a second value (R_{fill}) obtained from pixel values of pixels surrounding the given pixel so as to supply a decision signal (decR) if a predetermined criterion is

satisfied; and a multiplexer (115) coupled to said part (113, 117) for supplying the second value (R_{fill}) if the predetermined criterion is satisfied for not more than one color (R,G,B) and for supplying the first pixel value (R_{orig}) in the opposite case.

**FIG. 2****FIG. 3****EP 0 635 973 A1**

The invention relates to a signal correction for correcting deviating pixel values. Such a circuit is used, for example for correcting CCD sensors, some pixels of which have a dark current which is larger than average. Particularly in sensors having many pixels, there is a great risk of dropout of one or more pixels having a deviating dark current.

A signal correction circuit of this type is described in the article by B. Botte, "Digital automatic pixel correction in new generation CCD broadcast cameras", presented at the IBC 1992, pp. 474-478. In this circuit, a correction signal is corrected for the sensor temperature. To this end, the address of the faulty pixel should be known and, together with the magnitude of the correction, it should be stored in a memory. This circuit has the following drawbacks. The circuit is not flexible; if, in fact, new defects occur, the contents of the memory must be adapted. The temperature behaviour is probably not so predictable so that the corrections are not complete. Moreover, the dark current of the pixels may vary with respect to time with a too large dark current or even become normal. Then, there will be erroneous corrections.

It is, *inter alia* an object of the invention to provide an improved signal correction. To this end a first aspect of the invention provides a signal correction circuit as defined in claim 1. A second aspect of the invention provides a signal correction circuit as defined in claim 4. A third aspect of the invention provides a signal correction method as defined in claim 5. A fourth aspect of the invention provides a signal correction circuit as defined in claim 6. A fifth aspect of the invention provides a television camera as defined in claim 7. Advantageous embodiments are defined in the sub-claims.

In accordance with the first aspect of the invention, a prediction is made for each pixel value, starting from adjacent pixels, for example pixel pairs which are situated symmetrically with respect to the relevant pixel (pixels at the top and the bottom, left and right and also diagonally with respect to the relevant pixel). The pixel value to be corrected can then be limited, for example to the maximum of pairwise mean values of the adjacent pixel values. In this way the invention is capable of removing a pixel deviation from a video signal without the signal being noticeably affected.

The distinction between correct picture information and a defective pixel is based on the recognition that light incident on only a single pixel is most unlikely. An embodiment of the circuit according to the invention determines a prediction from neighbouring pairs by means of linear interpolation. The more neighbouring pairs are used, the better the discrimination. In one embodiment, six pairs, i.e. twelve neighbouring pixels are used. The measured pixel value is clipped at the maxi-

mum value of the six predictions. The advantage of this circuit is its adaptability to the situation. Each number and each value of single-pixel deviations can be corrected. The sole condition is that the pixels having a too large dark current must not be contiguous pixels, whereby preferably at least two correct pixels are present between two incorrect pixels, in the horizontal direction. It is possible to correct analog video signals in an analog manner. Alternatively, the correction may be performed on digital signals. Advantageously, the circuit according to the invention can be used in combination with the operation of generating horizontal and vertical contours because the same delayed signals are also necessary for this purpose.

A very attractive aspect of the invention is based on the recognition that the deviating pixels are very unlikely to coincide in the three color sensors of the same camera. When it is attempted by means of filters or other means to discriminate between information and defects for each individual color, there will be a great risk that information which seems to be a defect is also removed. Such misleading information which is to be maintained is constituted, for example by the glitters giving the image the impression of sharpness. Such glitters are simultaneously present in the three chrominance channels and may thus be distinguished from faulty pixels which occur in a single chrominance channel only. An analog elaboration thereof is to add an extra signal to a non-additive mixer circuit in the green chrominance channel, which additional signal consists of the sum of low-frequency green and high-frequency red. The sum of low-frequency green and high-frequency blue may be applied to a further input.

These and other aspects of the invention will be apparent from and elucidated with reference to the embodiments described hereinafter.

In the drawings

Fig. 1 shows a first embodiment of a signal correction circuit according to the invention;

Fig. 2 shows a configuration of neighbouring pixels to elucidate a 2-D filtering in the circuit of Fig. 1;

Fig. 3 shows an efficient form of a component of the circuit of Fig. 1; and

Fig. 4 shows a blue part of an analog signal correction circuit according to the invention.

The signal correction circuit of Fig. 1 uses two line delays 103, 105 denoted by H-del, per color R, G, B. Pixel values to be corrected are applied to the inputs Rin, Gin, Bin. The corrected pixel values can be taken from the outputs R1, G1 and B1. A pixel element having a deviating dark current can be recognized by comparing, per color, the pixel value of the relevant pixel with the pixel values of neighbouring pixels. This recognition is performed

by the 2-D filters 101 in Fig. 1. The pixel elements are corrected per color, but in accordance with the preferred embodiment of the invention shown in Fig. 1, information from the other chrominance channels is used to prevent small contours from being removed erroneously.

If a pixel value for a given color differs substantially from the pixel values of neighbouring pixels, and if this difference is not present in the other color channels, the pixel will be considered to be deviating and its value will be replaced by a value derived from the pixel values of neighbouring pixels. If, on the other hand, a deviation is detected in more than one chrominance channel at a time, it is assumed that this deviation represents a detail in the image and should thus not be removed. The decision to correct or not to correct is taken in the decision circuit 109, a part of which is shown in greater detail in Fig. 3. The correction circuit shown in Fig. 1 also performs some preprocessing operations for a contour correction module (not shown) which is also present in a camera by making the sum (the average) $R_{0,2}$, $G_{0,2}$, $B_{0,2}$ of the input signal and the input signal delayed by two line periods available for each color.

Fig. 2 shows a configuration of neighbouring pixels to elucidate the operation of the 2-D filters 101 in the circuit of Fig. 1. For each chrominance channel the values of neighbouring pixels are averaged along the lines shown in Fig. 2 by adding each time the two pixels located on one line and by dividing them by two. This results in six mean values, the largest of which, R_{filt} , G_{filt} , B_{filt} , together with the input signals R_{orig} , G_{orig} , B_{orig} are applied to the decision circuit 109.

As is shown in Fig. 3, a comparison circuit 113 in a part 111 of the decision circuit 109 checks for each color R, G, B whether the original pixel value R_{orig} , G_{orig} , B_{orig} is larger than the associated largest mean pixel value R_{filt} , G_{filt} , B_{filt} computed by the 2-D filter 101. If this is the case, the comparison circuit 113 supplies a flag signal flagR (flagG, flagB) to a decision combination circuit 117 which, when only a single flag signal has been supplied, assigns the associated multiplexer 115 (by means of decision signal decR, decG and decB) to supply the filtered pixel values R_{filt} , G_{filt} and B_{filt} instead of the original pixel values R_{orig} , G_{orig} , B_{orig} , respectively. Of course, the original pixel value is supplied for the colors for which no flag signal has been supplied. If more than one flag signal has been supplied, the original pixel value will be supplied for all colors. For the sake of simplicity, Fig. 3 shows the part 111R, the comparison circuit 113R and the multiplexer 115R for the color Red only; of course, corresponding circuit elements are present for the other colors Green and Blue.

Fig. 4 shows a blue part of an analog signal correction circuit according to the invention. The blue input signal B is applied to a series arrangement of two line delays 403 and 405. The blue input signal B is also applied to a series arrangement of four delay sections 419, 421, 423 and 425 which delay the blue input signal and the blue signal B(2H) delayed by two line periods each time by 15 ns. A period of 15 ns corresponds to the pixel space for HDTV signals; in normal definition television signals (MAC, PAL, SECAM, NTSC) a delay of 70 ns would have to be used. These 15 ns delay sections 419-425 operate in such a way that a signal applied "at the rear" will appear in a delayed form "at the front", and that a signal applied "at the front" will appear in a delayed form "at the rear". Hence, the sum of the blue input signal B and the signal B(2H+60ns) delayed by two line periods plus four times 15 ns is present at junction point P1. The sum of B(15ns) and B-(2H+45ns) is present at junction point P2. The sum of B(30ns) and B(2H+30ns) is present at junction point P3. The sum of B(45ns) and B(2H+15ns) is present at junction point P4. The sum of B(60ns) and B(2H) is present at junction point P5. The signals at the junction points P1-P5 are applied to a maximum circuit 427.

The signal B(1H) at the output of the line delay 403 is applied to an emitter-follower buffer EF v/a two 15 ns delay sections 429 and 431. A minimum circuit 435 determines the minimum of the output signal of the maximum circuit 427 and the output signal of the emitter-follower buffer EF. Similarly as in the digital embodiment described hereinbefore, the signal B(1H+30ns) at the output of the delay section 431 is limited at a given position in the image to the maximum of signals derived from signals at a plurality of neighbouring positions. Put in other words, the signal correction circuit of Fig. 4 comprises first means 419-427 for obtaining a second value MAX-B from pixel color values B, B-(15ns), B(30ns), B(45ns), B(60ns); B(2H), B-(2H+15ns), B(2H+30ns), B(2H+45ns), B-(2H+60ns) of pixels surrounding a given pixel having a first pixel value B(1H+30ns), and second means 435 for supplying the second value MAX-B if the first pixel value B(1H+30ns) is larger than the second value MAX-B. For correcting signals of different colors (R,G,B), the first means (419-427) include means (427) for providing the second value (MAX-B), in dependence on at least one further pixel color value (G) of the given pixel.

To avoid misinterpretation of detail information in only one color as a deviating pixel to be corrected, the embodiment of Fig. 4 also uses information of another color, viz. green (G). To this end, the pixel value B(1H) to be filtered is low pass-filtered in a low-pass filter 437 and subse-

quently added to a green signal G filtered by a high-pass filter 441; as a result it is achieved that the green detail information, which in fact is present in the high-frequency part of the green signal, is provided with the DC or luminance level associated with the blue signal. The sum signal of low-frequency blue and high-frequency green is subsequently applied to the maximum circuit 427. It is thereby achieved that the maximum MAX-B need not be adjusted at a lower value than a value associated with the green maximum if there is also a locally high pixel value in green, so that a locally high value in the blue signal is less rapidly cut off. If desired, the sum of low-frequency blue and high-frequency red may also be taken into account in determining the maximum. The circuit components (not shown) for the green and the blue signal are adapted to be such that in the green component the sum of low-frequency green and high-frequency red and/or the sum of low-frequency green and high-frequency blue is taken into account in determining the maximum. In the red component the sum of low-frequency red and high-frequency green and/or the sum of low-frequency red and high-frequency blue is taken into account in determining the maximum.

It is to be noted that the embodiments described hereinbefore are non-limitative and that those skilled in the art will be capable of designing many alternative embodiments without departing from the scope of the invention defined by the accompanying claims.

Claims

1. A signal correction circuit for correcting deviating pixel values, comprising:

first means (101R) for obtaining a second value (R_{filt}) from pixel values of pixels surrounding a given pixel having a first pixel value (R_{orig}); and

second means (109) for supplying the second value (R_{filt}) if the first pixel value (R_{orig}) is larger than the second value (R_{filt}), wherein for correcting signals of different colors (R, G, B), the second means (109) comprise means (111, 115, 117) for supplying the second value (R_{filt}) if the first pixel value (R_{orig} , G_{orig} , B_{orig}) for not more than one color is larger than the second value (R_{filt} , G_{filt} , B_{filt} , respectively).

2. A signal correction circuit as claimed in Claim 1, wherein the second means (109) comprise:

comparison means (113R) for each color (R, G, B) for comparing the first pixel value (R_{orig} , G_{orig} , B_{orig}) of the given pixel with the second value (R_{filt} , G_{filt} , B_{filt}) obtained from pixel values of pixels surrounding the given

pixel so as to supply a flag signal (flagR) if the first pixel value (R_{orig} , G_{orig} , B_{orig}) is larger than the second value (R_{filt} , G_{filt} , B_{filt} , respectively);

combination means (117) coupled to the comparison means (113R) for supplying a plurality of decision signals (decR, decG, decB) if the first pixel value (R_{orig} , G_{orig} , B_{orig}) for not more than one color is larger than the second value (R_{filt} , G_{filt} , B_{filt} , respectively); and

switching means (115R) coupled to said comparison means (113R) and combination means (117) for each color (R, G, B) for supplying the second value (R_{filt} , G_{filt} , B_{filt}) if the associated decision signal (decR, decG, decB) has been supplied, and for supplying the first pixel value (R_{orig} , G_{orig} , B_{orig} , respectively) in the opposite case.

3. A signal correction circuit as claimed in Claim 1, wherein the second value (R_{filt}) is constituted by the maximum of a plurality of mean values of pairs of pixels located diametrically with respect to the given pixel.

4. A signal correction circuit for correcting deviating pixel values of different colors (R, G, B), comprising:

first means (111R) for detecting, per color (R, G, B), deviations of pixel values;

combination means (117) coupled to said first means (111R) for supplying, per color (R, G, B), correction control signals (decR, decG, decB) on the basis of deviations (flagR, flagG, flagB) of the pixel values detected for different colors (R, G, B); and

correction means (115R) coupled to said combination means (117) for correcting the pixel values per color (R, G, B).

5. A method of correcting deviating pixels of different colors (R,G,B), comprising the steps:

detecting deviations of pixel values for each color (R,G,B) separately;

supplying correction control signals (decR, decG, decB) separately for each color (R,G,B) on the basis of detected deviations (flagR, flagG, flagB) of the pixel values detected for at least two colors (R,G,B); and

correcting deviating pixels on the basis of said correction control signals (decR, decG, decB) for each color (R,G,B).

6. A signal correction circuit for correcting deviating pixel values, comprising:

first means (419-427) for obtaining a second value (MAX-B) from pixel color values of pixels surrounding a given pixel having a first pixel color value; and

second means (435) for supplying the second value (MAX-B) if the first pixel color value is larger than the second value (MAX-B); wherein

for correcting signals of different colors (R,G,B), the first means (419-427) include means (427) for providing said second value (MAX-B) in dependence on at least one further pixel color value (G) of said given pixel.

7. A television camera comprising:

a pick-up unit for providing pixel values of three colors (R,G,B); and

a signal correction circuit for correcting deviating pixel values, wherein the signal correction circuit is defined in accordance with claims 1, 4 or 6.

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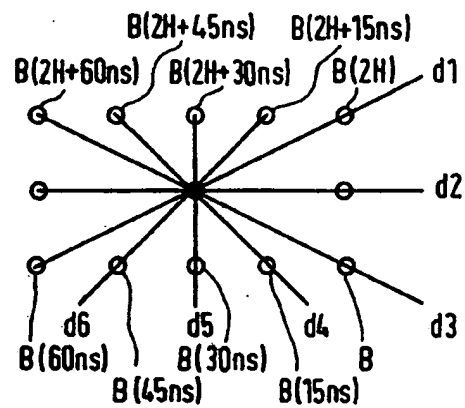
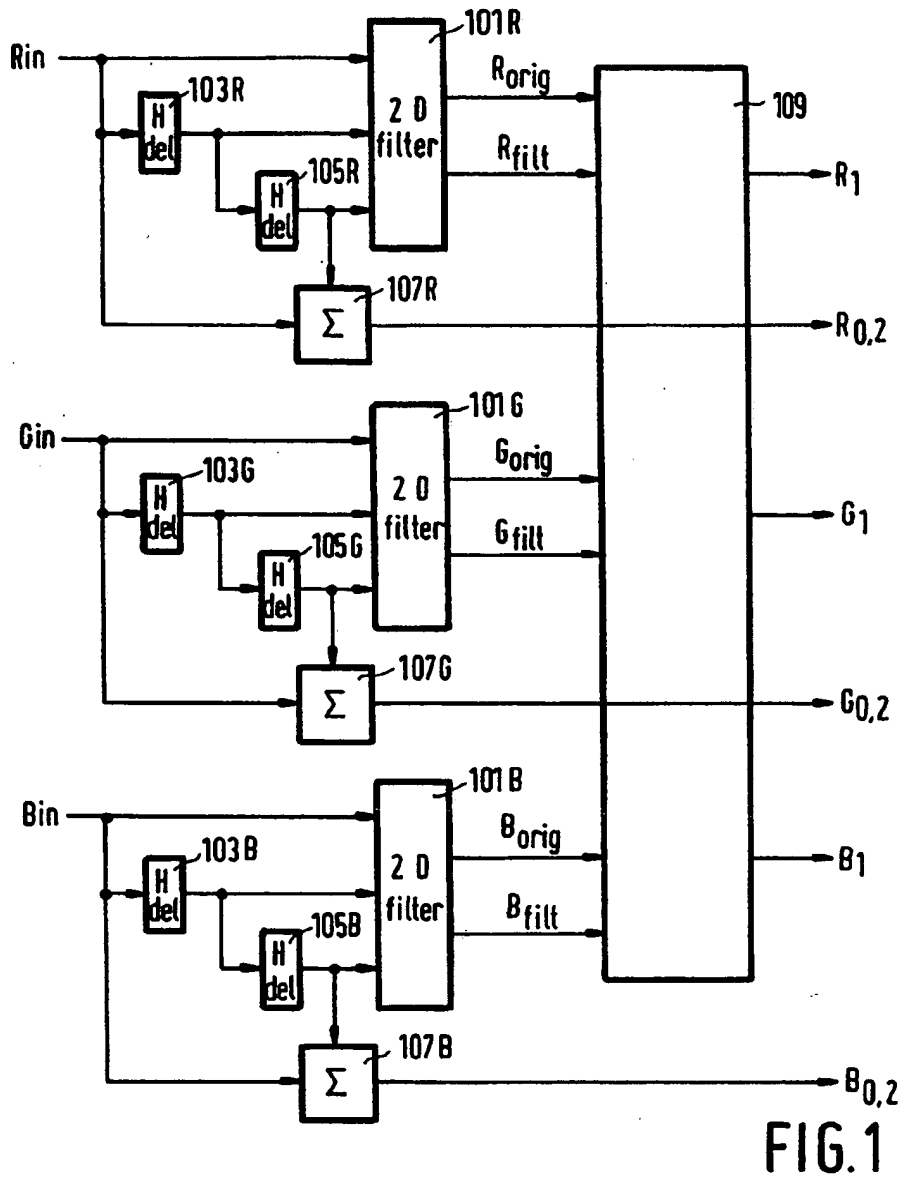
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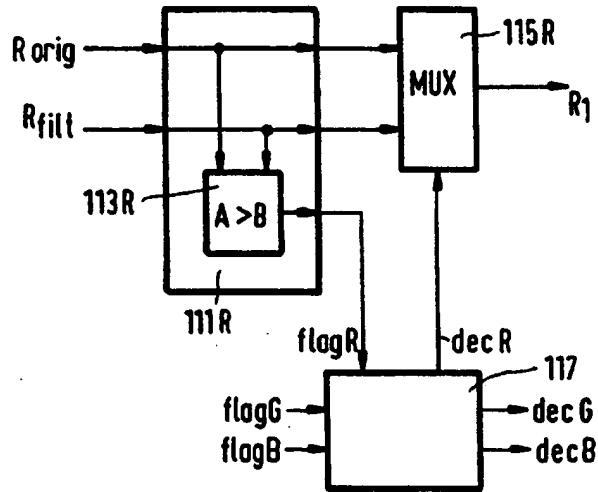


FIG. 3

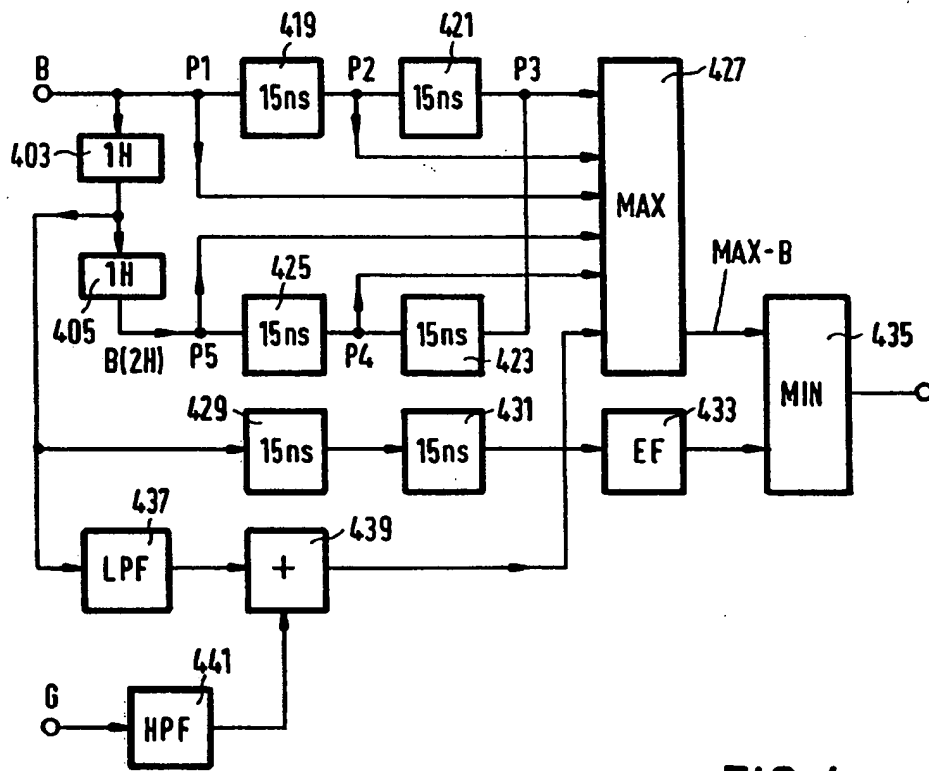


FIG. 4



| DOCUMENTS CONSIDERED TO BE RELEVANT | | | |
|---|---|--|--|
| Category | Citation of document with indication, where appropriate, of relevant passages | Relevant to claim | CLASSIFICATION OF THE APPLICATION (Int.Cl.6) |
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| X | DE-A-31 14 275 (ROBERT BOSCH GMBH) 4 November 1992 * page 8 - page 14 * * figures 1-4 * | 1-7 | |
| A | US-A-3 904 818 (KOVAC) 9 November 1975 * figures 5,8 * | 1,2,4,5,7 | |
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| The present search report has been drawn up for all claims | | | |
| Place of search THE HAGUE | | Date of completion of the search 30 August 1994 | Examiner Hoekstra, A |
| CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application I : document cited for other reasons & : member of the same patent family, corresponding document | | | |